| Electronic Acknowledgement Receipt | | |
|--------------------------------------|---|--|
| EFS ID: | 6034985 | |
| Application Number: | 10577526 | |
| International Application Number: | | |
| Confirmation Number: | 9839 | |
| Title of Invention: | Complementary Misfet And Integrated Circuit | |
| First Named Inventor/Applicant Name: | Yasuhiko Kasama | |
| Customer Number: | 00466 | |
| Filer: | Liam Joseph McDowell/Linda Sayyad | |
| Filer Authorized By: | Liam Joseph McDowell | |
| Attorney Docket Number: | 8075-1086 | |
| Receipt Date: | 09-SEP-2009 | |
| Filing Date: | 16-MAR-2007 | |
| Time Stamp: | 10:45:55 | |
| Application Type: | U.S. National Stage under 35 USC 371 | |

Payment information:

| Submitted with Payment | no | | |
|------------------------|----|--|--|
| File Listing: | | | |
| | | | |

| Document Number | Document Description | File Name | File Size(Bytes)/ Message Digest | Multi Part /.zip | Pages (if appl.) |
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| 1 | | 2009-09-09amendment.pdf | 38310 | yes | 10 |
| | | | ae907558afe154e2ec25dae1ee96b268c46b 1705 | | |

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| Applicant Arguments/Remarks Made in an Amendment | 6 | 10 | | |

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.